

CLAIM LISTING:

1-7. (Cancelled)

8. (Currently Amended) A system for displaying frames, said system comprising:
a display engine for providing a first frame;
a host processor for providing information about a second frame to the display engine, after the display engine provides the first frame; and
wherein the display engine provides the first frame, if host processor does not provide the information regarding the second frame to the display engine before ~~a predetermined time~~ a first horizontal synchronization pulse following a vertical synchronization pulse associated with the second frame.

9. (Original) The system of claim 8, wherein the display engine provides the second frame if the host processor provides the information regarding the second frame before the predetermined time.

10. (Cancelled)

11. (Original) The system of claim 8, further comprising:
a frame buffer for storing the second frame beginning at least one starting address; and
wherein the information comprises the at least one starting address.

12. (Currently Amended) The system of claim 8, further comprising:
a first at least one register for storing [[the]] information regarding the first frame.

13. (Original) The system of claim 12, further comprising:

a feeder for providing the first frame based on the information regarding the first frame if the host processor does not provide the information regarding the second frame before the predetermined time.

14. (Original) The system of claim 13, wherein the host processor overwrites the information regarding the first frame with the information regarding the second frame and wherein the feeder providing the second frame based on the information regarding the second frame.

15. (Original) The system of claim 13, wherein the feeder rasterizes the first frame.

16-20. (Cancelled)